

PTO-1449

Application No.

Applicant(s)

09/870,944

Michael I. Catherwood

**Information Disclosure Citation
in an Application**

Docket Number

Group Art Unit

Filing Date

068354.1443

2124

June 1, 2001

FEB 15 2005

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
		3930253	12/30/75	Malda	340	347	1/24/74
	B.	4323981	4/6/82	Nakamura	364	749	10/23/78
	C.	4615005	9/30/86	Maejima et al.	743	601	7/20/84
	D.	4742479	5/3/88	Kloker et al.	364	746	3/25/85
	E.	4768149	8/30/88	Konopik et al.	364	200	8/29/85
	F.	4800524	1/24/89	Roesgen	364	900	12/2/87
	G.	5034887	7/23/91	Yasui et al.	364	200	7/27/90
	H.	5040178	8/13/91	Lindsay et al.	371	21.5	5/12/89
	I.	5177373	1/5/93	Nakamura	307	265	9/27/91
	J.	5239654	8/24/93	Ing-Simmons et al.	395	800	11/17/89
	K.	5375080	12/20/94	Davies	364	736.5	12/18/92
	L.	5392435	2/21/95	Masui et al.	395	725	9/29/93

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	M.	01037124 A.	2/1989	JP (w/abstract)	H03M	001/82		X
	N.	96/11443	4/18/96	WO	G06F	15/78	X	
	O.	0 992 888	12/04/00	EP	G06F	0/32	X	
	P.	0 992 889	12/14/00	EP	G06F	9/32	X	

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	Q.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508					May 2001
	R.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, IS, vol. 37, no. 10, pages 46, 48, 50-53, XP000860706 ISSN:0010-4566					October 1998
	S.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515					2 March 1998
	T.	Intel, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, , Pages 3-1, 3-2, 3-15, 14-1 to 14-30, 18-7, and 25-289 to 25-292					1995
	U.	Intel, Embedded Intel486 Processor Family Developer's Manual, pgs. 2-2, 3-17, 3-37, 4-5, 4-6, 10-1 to 10-12, 12-1 to 12-10					10/1997
	V.	Moore, M "Z80 Family Interrupt Structure". Barleywood (online), retrieved from the internet <URL: http://www.gaby.de/z80/1653.htm>					1997
	W.	PCT Search Report based on PCT/US02/16706, 6 pages					Mailed 9/27/02

EXAMINER

DATE CONSIDERED

03/10/05

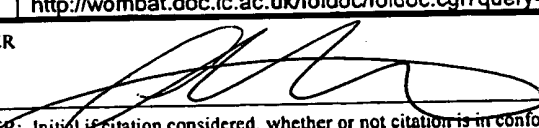
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PTO-1449		Application No. 09/870,944		Applicant(s) Michael I. Catherwood	
Information Disclosure Citation in an Application		Docket Number 068354.1443		Group Art Unit 2124	Filing Date June 1, 2001

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
①	A.	5418976	5/23/95	Iida	395	800	12/21/92
	B.	5432943	7/11/95	Mitsuishi	395	725	4/19/93
	C.	5504916	4/2/96	Murakami et al.	395	800	9/28/93
	D.	5525874	6/11/96	Mallarapu et al.	318	254	1/30/95
	E.	5561619	10/1/96	Watanabe et al.	364	736.5	9/20/94
	F.	5564028	10/8/96	Swoboda et al.	395	375	1/11/94
	G.	5611061	3/11/97	Yasuda	395	591	11/24/93
	H.	5649146	7/15/97	Riou	395	421.07	3/30/95
	I.	5659700	8/19/97	Chen et al.	395	421.07	2/14/95
	J.	5706466	1/6/98	Dockser	395	452	1/13/95
	K.	5740451	4/14/98	Muraki et al.	395	733	10/28/96
	L.	5748970	5/5/98	Miyaji et al.	395	733	4/30/96
	M.	5765218	6/9/98	Ozawa et al.	711	219	3/10/95
	N.	5778237	7/7/98	Yamamoto et al.	713	322	12/14/95
	O.	5812868	9/22/98	Moyer et al.	395	800.23	9/16/96
	P.	5815693	9/29/98	McDermott et al.	713	501	12/15/95
	Q.	5826072	10/20/98	Knapp et al.	395	387	11/13/95
①	R.	5867726	2/2/99	Ohsuga et al.	395	800.32	4/10/96

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	S.						
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NON-PATENT DOCUMENTS - DOCUMENT (Including Author, Title, Source, and Pertinent Pages)			
①	U.	PCT Search Report based on PCT/US02/16705, 7 pages	Mailed 9/9/02
①	V.	PCT Search Report based on PCT/US02/16921, 4 pages	Mailed 10/18/02
①	W.	SPARC, International, Inc., "The SPARC Architecture Manual", Version 8, pp 1-303	1992
①	X.	SPARC, International, Inc., "The SPARC Architecture Manual", Version 9, pp. 137-299	1994-2000
①	Y.	Free On-Line Dictionary of Computing (FOLDOC), http://wombat.doc.ic.ac.uk/foldoc/foldoc.cgi?query=program+counter	1995

EXAMINER 	DATE CONSIDERED 03/10/05
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

PTO-1449		Application No. 09/870,944		Applicant(s) Michael I. Catherwood	
Information Disclosure Citation in an Application <div style="border: 1px solid black; border-radius: 50%; width: 100px; height: 100px; display: flex; align-items: center; justify-content: center; margin: 10px auto;"> <div style="writing-mode: vertical-rl; transform: rotate(180deg);">OFFICE OF THE PATENT & TRADEMARK OFFICE</div> <div style="text-align: center;">FEB 15 2005</div> </div>		Docket Number 068354.1443		Group Art Unit 2124	Filing Date June 1, 2001
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	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
CD	A.	5875342	2/23/99	Temple	395 — 733	6/3/97
	B.	5900683	5/4/99	Rinehart et al.	307 — 126	12/23/97
	C.	5918252	6/29/99	Chen et al.	711 — 217	11/25/96
	D.	5937199	8/10/99	Temple	395 — 333	6/3/97
	E.	5974549	10/26/99	Golan	713 — 200	3/27/97
	F.	5978825	11/2/99	Divine et al.	708 — 525	11/14/97
	G.	5983333	11/9/99	Kolagotia et al.	711 — 219	8/27/97
	H.	5991868	11/23/99	Kamiyama et al.	712 — 32	5/19/94
	I.	6018757	1/25/00	Wong	708 — 525	8/8/96
	J.	6049858	4/11/00	Kolagotia et al.	711 — 217	8/27/97
	K.	6055619	4/25/00	North et al.	713 — 36	2/7/97
	L.	6084880	7/4/00	Bailey et al.	370 — 395	6/30/97
	M.	6181151	1/30/01	Wasson	324 — 765	10/28/98
	N.	6202163	3/13/01	Gabzdyl et al.	713 — 324	3/10/98
	O.	6209086	3/27/01	Chi et al.	712 — 244	11/10/98
	P.	6243786	6/5/01	Huang et al.	710 — 262	12/23/98
	Q.	6260162	7/10/01	Typaldos et al.	714 — 55	10/31/98
	R.	6282637	8/28/01	Chan et al.	712 — 223	12/2/98
	S.	6292866	9/18/01	Zaiki et al.	710 — 284	3/30/99
CD	T.	6315200	11/13/01	Silverbrook et al.	235 — 454	7/10/98

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	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
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NON-PATENT DOCUMENTS - DOCUMENT (Including Author, Title, Source, and Pertinent Pages)		
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EXAMINER 	DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive;">03/10/05</div>
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PTO-1447

**Information Disclosure Citation
in an Application**

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09/870,944

Applicant(s)

Michael I. Catherwood

Docket Number

068354.1443

Group Art Unit

2124

Filing Date

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U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.	6356970	3/12/02	Killian et al.	710	269	5/28/99
B.	6397318	5/28/02	Peh	711	220	4/2/98
C.	2002/0194466	12/19/02	Catherwood et al.	712	241	6/1/01
D.	2003/0093656	5/15/03	Masse et al.	712	241	10/1/99
E.	6658578	12/2/03	Laurenti et al.	713	324	10/1/99
F.	6681280	1/20/04	Miyake et al.	710	261	10/4/00
G.	6728856	4/27/04	Grosbach et al.	711	202	6/1/01
H.	6763478	7/13/04	Vinh X. Bui	713	600	10/24/00
I.	6751742	6/15/04	Duhault et al.	713	323	8/31/99
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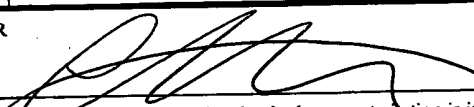
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CD	A.	5701493	12/23/97	Jaggar	395	734	8/3/95
CD	B.	5386563	1/31/95	Thomas, deceased	305	650	10/13/92
CD	C.	6694398	2/17/04	Zhao et al.	740	260	4/30/01
CD	D.	5623646	4/22/97	Clarke	305	560	9/12/95
CD	E.	4709324	11/24/87	Kloker	364	200	11/27/85
CD	F.	5561384	10/1/96	Reents et al.	327	108	11/8/95
CD	G.	5450027	9/12/95	Gabara	326	08	4/8/94
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